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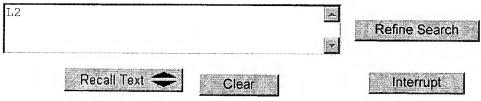
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| (configur\$5 near5 ROM) same (serial adj1 bus) | 38 |

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DATE: Monday, June 21, 2004 Printable Copy Create Case

Set Name Query
side by sideHit Count Set Name
result setDB=USPT, USOC; PLUR=YES; OP=ORL2 (configur\$5 near5 ROM) same (serial adj1 bus)38 L2

(configur\$5 near5 ROM) same (serial adj1 bus) same layer

END OF SEARCH HISTORY

L1

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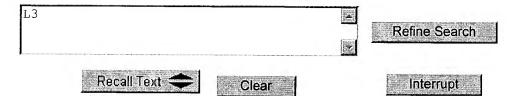
Search Results -

| Terms | Documents |
|-------|-----------|
| L2 | 0 |

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| $DB=EPAB,JPAB,DWPI,TDBD;\ PLUR=YES;\ OP=OR$ | | | | |
| <u>L3</u> L2 | 0 | <u>L3</u> | | |
| DB=USPT,USOC; PLUR=YES; OP=OR | | | | |
| <u>L2</u> (configur\$5 near5 ROM) same (serial adj1 bus) | 38 | <u>L2</u> | | |
| L1 (configur\$5 near5 ROM) same (serial adj1 bus) same layer | 7 | <u>L1</u> | | |

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| (358/1.15 370/463 709/253 709/301 709/302 709/220 710/104 710/105 710/106 710/62 710/63 710/2 710/305 710/8 714/1).ccls. | 6337 | | | | |

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L4

Refine Search
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| Set Name Query side by side | <u>Hit</u> Count | Set Name result set |
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| <u>L4</u> 710/104,105,106,62,63,2,305,8;709/253,301,302,220;370/463;714/1;358/1.15.ccls | . 6337 | <u>L4</u> |
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| <u>L3</u> L2 | 0 | <u>L3</u> |
| $DB=USPT,USOC;\ PLUR=YES;\ OP=OR$ | | |
| L2 (configur\$5 near5 ROM) same (serial adj1 bus) | 38 | <u>L2</u> |
| L1 (configur\$5 near5 ROM) same (serial adj1 bus) same layer | 7 | <u>L1</u> |

END OF SEARCH HISTORY

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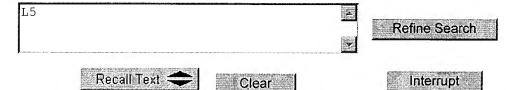
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|-----------|-----------|--|--|--|--|
| L2 and L4 | 21 | | | | |

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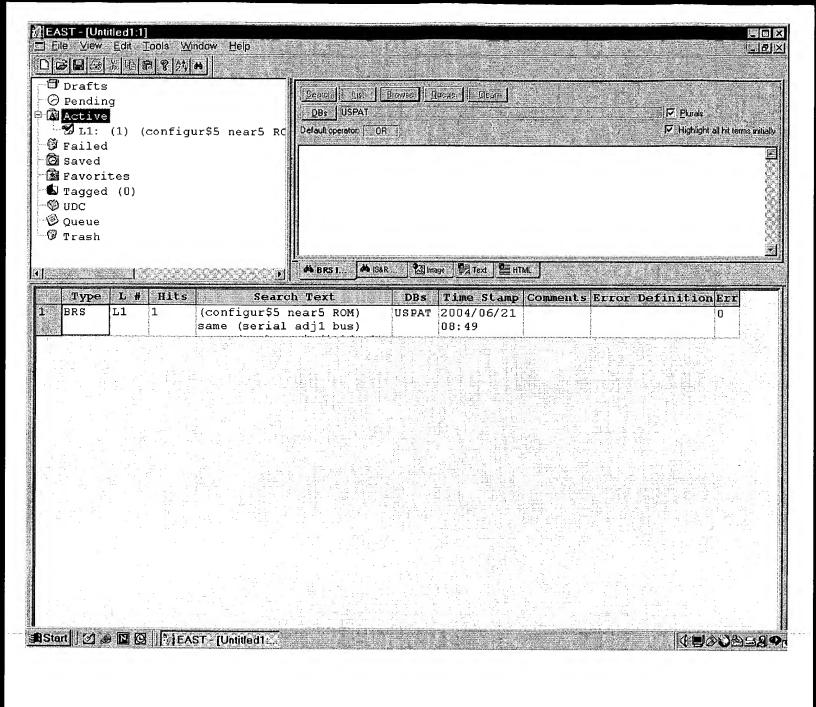


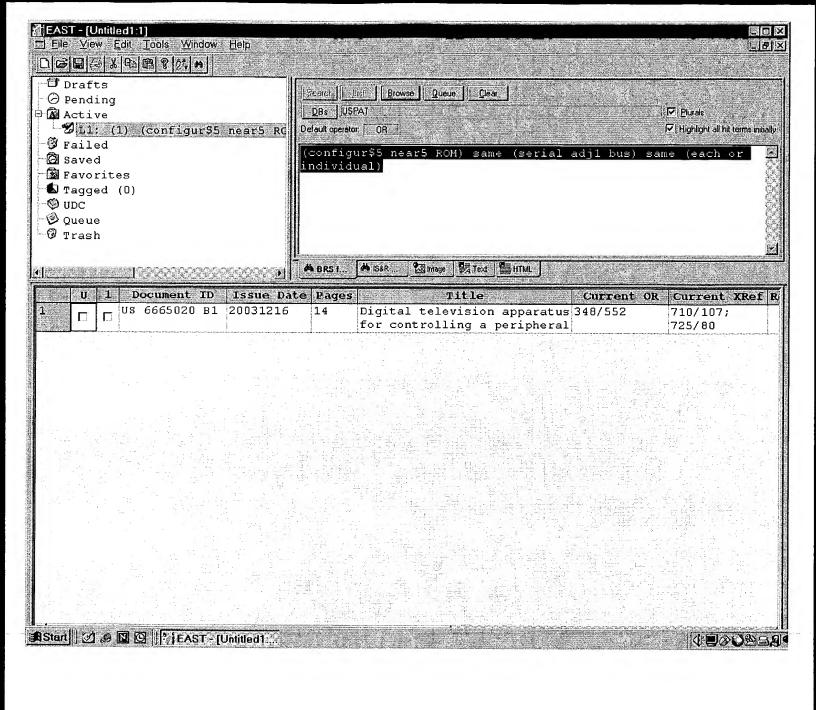
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| <u>L5</u> 12 and L4 | 21 | <u>L5</u> |
| <u>L4</u> 710/104,105,106,62,63,2,305,8;709/253,301,302,220;370/463;714/1;358/1.15.ccls. | 6337 | <u>L4</u> |
| $DB=EPAB,JPAB,DWPI,TDBD;\ PLUR=YES;\ OP=OR$ | | |
| <u>L3</u> L2 | 0 | <u>L3</u> |
| DB=USPT,USOC; PLUR=YES; OP=OR | | |
| <u>L2</u> (configur\$5 near5 ROM) same (serial adj1 bus) | 38 | <u>L2</u> |
| <u>L1</u> (configur\$5 near5 ROM) same (serial adj1 bus) same layer | 7 | <u>L1</u> |

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| O- By Author O- Basic O- Advanced | 1 Three-phase four wire voltage controlled AC line conditioner with uinput power factor and minimised output voltage harmonics Lo, YK.; Chen, CL.; |
| Member Services | Electric Power Applications, IEE Proceedings- , Volume: 142 , Issue: 1 , Jan. |
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Engineering in Medicine and Biology Society, 2000. Proceedings of the 22nd / International Conference of the IEEE , Volume: 4 , 23-28 July 2000 Pages: 2611 - 2614 vol.4

[Abstract] [PDF Full-Text (544 KB)] IEEE CNF

3 Requirements, configuration management and traceability for safet critical software

Romanski, G.;

Requirements Engineering Conference, 2003. Proceedings. 11th IEEE International, 8-12 Sept. 2003 Pages:304

[Abstract] [PDF Full-Text (175 KB)] IEEE CNF

4 A parallel ultra-high resolution MPEG-2 video decoder for PC cluster based tiled display systems

Han Chen; Kai Li; Bin Wei;

Parallel and Distributed Processing Symposium., Proceedings International, If 2002, Abstracts and CD-ROM, 15-19 April 2002

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Requirements, configuration management and trace for safety critical software

Romanski, G. Verocel, Inc.

This paper appears in: Requirements Engineering Conference, 2003. Pro 11th IEEE International

Publication Date: 8-12 Sept. 2003

On page(s): 304 ISSN: 1090-705X

Number of Pages: xv+378

Inspec Accession Number: 7914023

Abstract:

Software requirements are the focal point from which traceability to all related are established during the certification of safety critical software. For the certi air-borne software, the guidance document DO-178B, requires that the link b requirements, design, code and tests be documented and verified. The DO-17 document does not describe how this should be done, but it permits the reent information that is missing, to support the certification of commercial-off-the-(COTS) products. Requirements were entered in a database and evolved through sequence that enforced the states described in company process documents. descriptions, source code, tests, results and so on were maintained in a confi management (CM) system. The final audit was successful and the CD-ROM do requirements based certification package was accepted and commended. The the approach and the lessons learned were presented.

Index Terms:

formal specification formal verification military computing safety-critical software soft systems analysis CM system COTS product air-borne software certification package commercial-off-the-shelf company process document configuration management in reengineering safety critical software software requirement

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File: USPT

Jun 1, 2004

US-PAT-NO: 6745256

DOCUMENT-IDENTIFIER: US 6745256 B2

TITLE: Information processing apparatus and method

DATE-ISSUED: June 1, 2004

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Suzuki; Naohisa Yokohama JΡ Nakamura; Atsushi Kawasaki JP Kobayashi; Makoto Yokohama JΡ Katano; Kiyoshi Chiba JР

US-CL-CURRENT: 710/18; 710/19, 710/8

. Full Title Citation Front Review Classification Date Reference Basic S. Alt Constants Claims KWIC Draw. De

☐ 2. Document ID: US 6665020 B1

L1: Entry 2 of 7

File: USPT

Dec 16, 2003

US-PAT-NO: 6665020

DOCUMENT-IDENTIFIER: US 6665020 B1

TITLE: Digital television apparatus for controlling a peripheral device via a

digital bus

Full Title Citation Front Review Classification Date Reference SCORCOS Affection (Section 1998) Claims KWC Draw, Da

☐ 3. Document ID: US 6477589 B1

L1: Entry 3 of 7

File: USPT

Nov 5, 2002

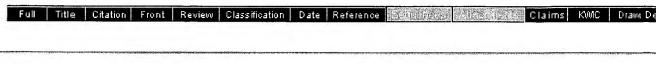
US-PAT-NO: 6477589

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DOCUMENT-IDENTIFIER: US 6477589 B1

** See image for Certificate of Correction **

TITLE: Information processing apparatus and method



☐ 4. Document ID: US 6367026 B1

L1: Entry 4 of 7

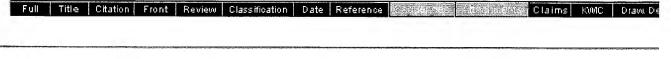
File: USPT

Apr 2, 2002

US-PAT-NO: 6367026

DOCUMENT-IDENTIFIER: US 6367026 B1

TITLE: Unbalanced clock tree for a digital interface between an IEEE 1394 serial bus system and a personal computer interface (PCI)



5. Document ID: US 6185622 B1

L1: Entry 5 of 7

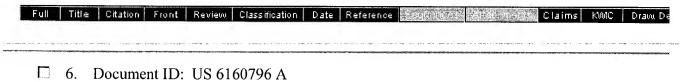
File: USPT

Feb 6, 2001

US-PAT-NO: 6185622

DOCUMENT-IDENTIFIER: US 6185622 B1

TITLE: Electronic apparatus, communication speed information collection method, communication method between electronic apparatus and recording medium



L1: Entry 6 of 7

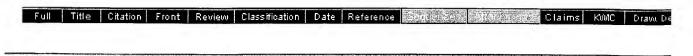
File: USPT

Dec 12, 2000

US-PAT-NO: 6160796

DOCUMENT-IDENTIFIER: US 6160796 A

TITLE: Method and system for updating device identification and status information after a local bus reset within a home audio/video network



☐ 7. Document ID: US 6038625 A

L1: Entry 7 of 7

File: USPT

Mar 14, 2000

US-PAT-NO: 6038625

DOCUMENT-IDENTIFIER: US 6038625 A

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 $\hbox{\tt TITLE: Method and system for providing a device identification mechanism within a consumer audio/video network}$

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Dram De |
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Jun 1, 2004

US-PAT-NO: 6745256

DOCUMENT-IDENTIFIER: US 6745256 B2

TITLE: Information processing apparatus and method

DATE-ISSUED: June 1, 2004

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Suzuki; Naohisa Yokohama JΡ Nakamura; Atsushi Kawasaki JΡ Kobayashi; Makoto Yokohama JΡ Katano; Kiyoshi Chiba JΡ

US-CL-CURRENT: 710/18; 710/19, 710/8

Full Title Citation Front Review Classification Date Reference (2015) 2015 2010 Claims KWIC Draws De

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File: USPT

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US-PAT-NO: 6665020

DOCUMENT-IDENTIFIER: US 6665020 B1

TITLE: Digital television apparatus for controlling a peripheral device via a

digital bus

Full Title Citation Front Review Classification Date Reference Could con 24th Stringers Claims KWC Draw. De

☐ 3. Document ID: US 6477589 B1

L1: Entry 3 of 7

File: USPT

Nov 5, 2002

US-PAT-NO: 6477589

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DOCUMENT-IDENTIFIER: US 6477589 B1

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TITLE: Information processing apparatus and method

Full Title Citation Front Review Classification Date Reference 34,000 4,000 Claims KMC Draw. De

4. Document ID: US 6367026 B1

L1: Entry 4 of 7

File: USPT

Apr 2, 2002

US-PAT-NO: 6367026

DOCUMENT-IDENTIFIER: US 6367026 B1

TITLE: Unbalanced clock tree for a digital interface between an IEEE 1394 serial bus system and a personal computer interface (PCI)

Full | Title | Citation | Front | Review | Classification | Date | Reference | Society | Alto 16 (1605) | Claims | KMC | Draw, De

☐ 5. Document ID: US 6185622 B1

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File: USPT

Feb 6, 2001

US-PAT-NO: 6185622

DOCUMENT-IDENTIFIER: US 6185622 B1

TITLE: Electronic apparatus, communication speed information collection method,

 $\hbox{\it communication method between electronic apparatus and recording medium} \\$

Full Title Citation Front Review Classification Date Reference Security 25 Table Claims KVMC Draw. De

☐ 6. Document ID: US 6160796 A

L1: Entry 6 of 7

File: USPT

Dec 12, 2000

US-PAT-NO: 6160796

DOCUMENT-IDENTIFIER: US 6160796 A

TITLE: Method and system for updating device identification and status information

after a local bus resét within a home audio/video network

Full | Title | Citation | Front | Review | Classification | Date | Reference | Deguispets | Etterative test | Claims | KMC | Draw De

☐ 7. Document ID: US 6038625 A

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File: USPT

Mar 14, 2000

US-PAT-NO: 6038625

DOCUMENT-IDENTIFIER: US 6038625 A

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 ${\tt TITLE:}$ Method and system for providing a device identification mechanism within a consumer audio/video network

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | (1,5) | | Claims | KWAC | Draw, De |
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File: USPT

Dec 30, 2003

US-PAT-NO: 6671768

DOCUMENT-IDENTIFIER: US 6671768 B1

TITLE: System and method for providing dynamic configuration ROM using double image

buffers for use with serial bus devices

DATE-ISSUED: December 30, 2003

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Brown; Steven W.

San Jose

CA

ASSIGNEE-INFORMATION:

NAME

CITY

STATE ZIP CODE

COUNTRY

TYPE CODE

02

Apple Computer, Inc.

Cupertino CA

APPL-NO: 09/ 431409 [PALM] DATE FILED: November 1, 1999

INT-CL: [07] $\underline{G06}$ \underline{F} $\underline{12/00}$, $\underline{G06}$ \underline{F} $\underline{13/00}$

US-CL-ISSUED: 711/102; 711/5, 711/170, 713/1, 713/100 US-CL-CURRENT: 711/102; 711/170, 711/5, 713/1, 713/100

FIELD-OF-SEARCH: 711/170, 711/102, 711/103, 711/5, 711/162, 710/101, 710/103,

710/104, 717/11, 714/6, 714/7, 713/1, 713/2, 713/100

PRIOR-ART-DISCLOSED:

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| | | | |
| PAT-NO | ISSUE-DATE | PATENTEE-NAME | US-CL |
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| 5701492 | December 1997 | Wadsworth et al. | 717/11 |
| 5809331 | September 1998 | Staats et al. | 710/10 |
| <u>5835761</u> | November 1998 | Ishii et al. | 713/2 |
| 5938764 | August 1999 | Klein | 713/1 |
| 5987605 | November 1999 | Hill et al. | 713/2 |
| 6073206 | June 2000 | Piwonka | 711/103 |

 ☐
 6141767
 October 2000
 Hu et al.
 714/1

 ☐
 6167532
 December 2000
 Wisecup
 714/23

ART-UNIT: 2187

PRIMARY-EXAMINER: Gossage; Glenn

ATTY-AGENT-FIRM: Sierra Patent Group, Ltd.

ABSTRACT:

A system and method for providing dynamic configuration Read Only Memory (ROM) using double image buffers for use with serial bus devices. The dynamic configuration ROM may be updated while linked to the serial bus and with little or no risk of publishing inconsistent configuration ROM information to other nodes on the bus. The dynamic configuration ROM comprises first and second configuration ROM images, one set to active, the other set to update. The dynamic configuration ROM publishes the configuration entries from the active configuration ROM image. Modifications to the configuration ROM are stored in a database. The update configuration ROM image is constructed from entries made to the database. After the construction of the update configuration ROM image is completed, the dynamic configuration ROM switches the states of the ROM images and transmits a serial bus reset signal. Subsequently, the configuration entries from the newly active configuration ROM images are then published to the other nodes on the bus.

10 Claims, 6 Drawing figures